



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: G.R. Mohan Rao

Serial No.: 10/665,906

Filing Date: September 18, 2003

Art Unit: 2818

Title: MEMORIES FOR ELECTRONIC SYSTEMS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Applicant hereby submits the following references in accordance with 37 C.F.R. §§ 1.56, 1.97 and 1.98. Copies of the references cited in the attached PTO/SB/08A are not enclosed nor required. Furthermore, pursuant to 37 C.F.R. § 1.97(g) and (h), no representation is made that this is material to patentability of the present application or that a search has been made.

Applicant hereby submits that claims of Applicant's above-referenced patent application are patentably distinguishable from these references.

It is believed that no fees are due; however, the Director of Patents and Trademarks is hereby authorized to charge any fees relating to this Information Disclosure Statement under 37 CFR § 1.17 to Deposit Account No 23-2426 of WINSTEAD SECHREST & MINICK P.C.

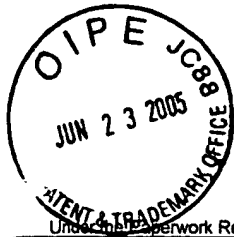
Respectfully submitted,



Ruben C. DeLeon
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Date: June 21, 2005

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PTO/SB/08A (04-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/665,906
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First Named Inventor	G.R. Mohan Rao
Art Unit	2818
Examiner Name	
Attorney Docket Number	17200-P043US

Sheet 1 of 8

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 3,460,120	08-05-1969	Lichowsky, Abraham	
		US- 5,440,523	08-08-1995	Joffe, Alexander	
		US- 5,732,041	03-24-1998	Joffe, Alexander	
		US- 5,784,582	07-21-1998	Hughes, John H.	
		US- 5,835,932	10-10-1998	Rao, G.R. Mohan	
		US- 5,856,940	01-05-1999	Rao, G.R. Mohan	
		US- 6,381,668	04-30-2002	Lunteren, Jan Van	
		US- 6,381,715	04-30-2002	Bauman, et al.	
		US- 6,415,332	07-02-2002	Tuel, Jr., William G.	
		US- 6,418,063	07-09-2002	Seitsinger, et al.	
		US- 6,459,647	10-01-2002	Kengeri, Subramani	
		US- 6,473,838	10-29-2002	Bass, et al.	
		US- 6,477,631	11-05-2002	Martin, et al.	
		US- 6,493,346	12-10-2002	Cantwell, et al.	
		US- 6,504,785	01-07-2003	Rao, G.R. Mohan	
		US- 6,519,675	02-11-2003	Manning, Troy A.	
		US- 6,519,689	02-11-2003	Manning, Troy A.	
		US- 6,545,936	04-08-2003	Yoo, et al.	
		US- 6,560,229	05-06-2003	Kadambi, et al.	

FOREIGN PATENT DOCUMENTS

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	70	IRENE ALDRIDGE, "Analysis of existing wireless communication protocols", Wireless/Mobile Communications, Columbia University, 2000	
	2	P.W. DIODATO, "Embedded DRAM: More than just a memory", IEEE Communications Magazine, July 2000.	
	13	ZHAO ZANG ET AL, "A permutation-based Page interleaving scheme to reduce Row-buffer conflicts and exploit data locality", Proceedings of the 33 rd Annual International Symposium on Micro-architecture, Micro-33, Monterey, California, Dec. 10-13, 2000	
	14	WI FEN LIN, ET AL, "Reducing DRAM latencies with an integrated memory hierarchy design", 7th International Symposium on high-performance computer architecture, January 2001.	
	37	BRIAN DIPERT, "Exotic memories, diverse approaches", EDN Magazine, April 26, 2001.	
	15	WEI FEN LIN, ET AL, "Designing a modern memory hierarchy with hardware Prefetching", IEEE Transactions on Computers, Vol. 50, No 1 1, November 2001.	
	63	MICHAEL CHUNG, RAMBUS, INC., "Logic Interface Solutions for Networking Applications", (www.rambus.com/rdf/ rdf 2002/ pdf/ 5_Line cord.pdf view as html:)	
	12	ZHAO ZHANG ET AL., "Breaking address Mapping Symmetry at multi-levels of memory hierarchy to reduce DRAM Row-buffer conflicts", The Journal of Instruction-level, parallelism, Vol. 3, 2002.	
	10	PETER RICHMOND, "Work Cords Evaluate Memory Options carefully when choosing Next-gen", Electronic News, March 18, 2002.	
	20	KEVIN KILBUCK, "FCRAM Parts 1 and 2", Communication Design, March 18, 2002 and March 26, 2002	

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NON PATENT LITERATURE DOCUMENTS			
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	28	SAMSUNG ELECTRONICS, Samsung Network•DRAM for Network Applications, April 2002	
	3	A. COSOROABA, "Memory options explode for Network Processors", Communication System Design, May 1, 2002	
	23	COLE, B, "Developers must balance NPU Programmability and performance issues", EE Times, Page 5, August 5, 2002	
	62	INTEL, Intel Technology Journal, Network Processors, Vol 6, Issue 3, August 15, 2002	
	71	LATTICE SEMICONDUCTOR CORPORATION, DDR SDRAM controller data sheets, 2003	
	72	LATTICE SEMICONDUCTOR CORP, "Lattice Announces Eureka Technology Partners IP Cores", Page 2, Sept. 2, 2003	
	17	MITSUBISHI ELECTRIC, Mitsubishi M5M5Y5672T•G•25,22,20,18,874,368•bit Network SRAM data sheet, preliminary, Rev 0.8, Page 2, January 14, 2003	
	35	PETER SINGER, "FRAM: The new contender", Semiconductor International, February 1, 2003	
	52	JIM GEIER, "Understanding WLAN routers", www.internet.com, February 26, 2003	
	36	LINDA GEPPERT, "The new indelible memories", IEEE Spectrum, Page 2, March 2003	

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	11	GILDER.COM, Memory, Storage and Untethered Devices, Gilder Technology Report, March 2003	
	50	SAMSUNG SEMICONDUCTOR GLOBAL, Samsung K4C5608/ 1638C, 256 Mb Network-DRAM Specification, Rev 0.6, April 2003	
	30	L. WIRBEL, "Earnings restated, Riversstone now faces delisting", EE Times, April 8, 2003	
	4	KIRAN PURANICK, "Digging into Advanced Switching Spec.", Communication Design (Coms.Design.com), April 10, 2003	
	25	A.D. BOVOPOULOS, ET AL., "Shared-memory fabrics meet 10-Gbit backplane demands", Communications Design, April 23, 2003	
	21	CHAPPELLE BROWN, "Overview", Page 2, EE Times, April 28, 2003	
	1	EMBEDDED SYSTEMS ENGINEERING, Evaluating Performance of memory in networking applications, May 2, 2003	
	60	DENNIS BUSSAGLIA, ET AL., "Addressing System level challenges in High Speed Comm chips", Comms Design.com, May 8, 2003	
	61	PETER CLARKE, "Nantero reports 10 Gbit nanotube memory array", Semiconductor Business News, May 8, 2003	
	64	EE TIMES UK, "Opticam plastic memory hits volume manufacturing hurdle", Nortrade.com, May 12, 2003	

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	66	A. SHILOV, "Infineon and Micron announce RDRAM II Spec.", EBN (Electronic Buyers News), May 12, 2003	
	67	ELECTROSPEL, "Fujitsu claims industry first in 10Gbits/s ethernet chips", Electronic Buyer News online, May 14, 2003	
	68	E. CHANG, ET AL., "RLDRAMs vs CAMs/SRAMs: Part 1", CommsDesign.com, June 3, 2003	
	73	E. CHANG, ET AL., "RLDRAMs vs CAMs/SRAMs: Part 2", COMMsDesign.com; June 9, 2003	
	74	DARRELL DUNN, "IDT'S flow-control IC's aid network systems", EBN (Electronic Buyers News), June 9, 2003	
	75	CHO, ET AL., "A Novel Cell Technology for Phase Change RAM", Samsung Electronics Co. Ltd., June 24, 2003	
	16	SONIC INC., MemMax™ Memory Scheduler (web site)	
	18	IDT, IDT 512k x 36, 1M x 18 2.5V Synchronous ZBT™ SRAM'S, data sheet	
	19	PAT LASSERRE, "IDT Application brief AN-203, "ZBTSRAMS: System design issues and bus timing", Integrated Device Technologies, Inc., June 2001	
	22	EZ CHIP TECHNOLOGIES, IPV6 Whitepaper.1 (web site) "IPV6 to IPV4 is not merely 50% more"	

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	24	S. LYER, ET AL. "Designing Packet Buffers for Router Linecards"	
	69	PROTOCOLS.COM, "VoIP Call Statistics", www.protoeols.com	
	5	CISCO, INC., Cisco 12000 Series, One port 10-Gigabit Ethernet Line Card, Data Sheet	
	6	CISCO, INC., Cisco 12016 Gigabit Switch Router, Data Sheet, July 3, 2000	
	7	CISCO, INC., "Cisco 12000 Series Internet Routers", Data Sheet, 2005	
	8	ANTANTICA, INC., 12000 Series of Gigabit Switch Routers (GSR)", Page 2, 2005	
	9	CISCO SYSTEMS, "Dynamic Packet Transport (DPT) Line Card", Data Sheet	
	58	CYPRESS SEMICONDUCTOR, INC., CY9C62256, 32k x 8 Magnetic Nonvolatile CMOSRAMC, Preliminary data sheet	
	27	INFINEON TECHNOLOGIES, RL DRAM Reduced Latency DRAM, Dec. 5, 2003	
	29	MICRON, MT46V32M4, DDRSDRAM data sheet, May 16, 2005	

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	48	MICRON TECHNOLOGY, Micron 288Mb SIO RLDRAM II, MT49H16M 18C, Advance data sheet, 2005	
	49	MICRON TECHNOLOGY, Micron MT49H8M32, RLDRAM data sheet, 2005	
	51	MICRON TECHNOLOGY, Micron 288 Mb CIO RLDRAM II, MT49H8M 36, data sheet, 2005	
	59	RAMTRON INTERNATIONAL, FM30C256, 256Kb Data Collector, data sheet (Ramtron Web Site), 2005	
	57	SAMSUNG SEMICONDUCTOR, INC., Samsung K9K1G08UOM*YCBO, 128 M x 8 bit NAND flash memory, data sheet, April 2005	
	26	TOSHIBA AMERICA ELECTRONIC COMPONENTS, INC., "Network FCRAM Speeds Up Performance, FREE Develop Up Your Design Cycle", 2005	

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